

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO. : 7,701,059 B1  
APPLICATION NO. : 08/915658  
DATED : April 20, 2010  
INVENTOR(S) : Jigish D. Trivedi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

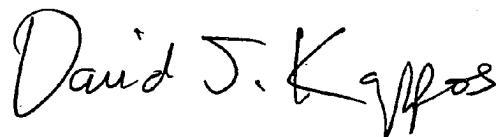
In column 10, line 4, in Claim 2, delete "silicide," and insert -- silicide --, therefor.

In column 10, line 38, in Claim 7, delete "said least one" and insert -- said at least one --, therefor.

In column 10, line 55, in Claim 8, delete "transistor, and" and insert -- transistor; and --, therefor.

Signed and Sealed this

Eighth Day of June, 2010



David J. Kappos  
Director of the United States Patent and Trademark Office